

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR**  
ANANTHAPURAMU - 515 002, ANDHRA PRADESH, INDIA

Sl. No. PC 0281719

Adm. No.10PH0220

Aadhaar No:761901257987



**PROVISIONAL CERTIFICATE**

*This is to certify that Mr. N RAJASEKHARVARMA*

*Son of Sri N SATYANARAYANA &*

*Smt Late N JAYA LAXMI,*

*has satisfied all the requirements for the award of*

**\*\*\* DOCTOR OF PHILOSOPHY \*\*\***

*in the faculty of \*\*\* Electrical Engineering \*\*\**

*for the Thesis entitled,*

**" WAVELET TRANSFORM BASED HIGH IMPEDANCE FAULT DETECTION IN  
DISTRIBUTION SYSTEM USING ARTIFICIAL INTELLIGENCE AND KNN  
TECHNIQUES "**

*(Date of Viva Voce : 31.05.2019)*

*Thursday, 20 June, 2019.*

**DIRECTOR OF EVALUATION**

**REGISTRAR**





E-Mail ID: drnd.vivavoce@jntua.ac.in  
Phone & Fax: 08554-273006  
Mobile: 9000 551420

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR**  
**ANANTHAPURAMU-515 002(A.P) INDIA**  
**Research & Development**

**Dr. A. Ananda Rao**  
Professor of CSE &  
Director

**CONFIDENTIAL**

To

**Lr.Rc.No.603-18/Ph.D.823/NRV/EEE/2018, Dt :28.03.2019**

1. **Dr. B.V. Sanker Ram,**  
Professor,  
Dept. of Electrical & Electronics Engineering,  
JNTU College of Engineering, Kukatpally,  
Hyderabad-500085  
Cell: **8500961061**  
Email Id: **bvsram4321@yahoo.com**

**[Internal Examiner]**

2. **Dr. Yogendra Kumar,**  
Professor, Dept. of EE,  
Moulana Nataional Institute of Technology,  
Bhopal-462003  
Cell: **08959662996**  
Email Id: **ykmact@yahoo.co.in**

**[External Examiner]**

Sir,

Sub: JNTUA – R & D – Ph.D. (EE) – Appointment of  
Examiners – Request for date of Viva-Voce examination – Reg.

Ref: D.O.Lr.No.660/ Ph.D./EEE-23/NRVM/EE /2018, dt: 27.03.2019.

\*\*\*

I am happy to inform you that you have been appointed as examiner for the conduct of Ph.D. Viva-Voce examination in respect of **Mr. N. Rajasekhar Varma** in the faculty of **Electrical & Electronics Engineering.**

Therefore, I request you to communicate any two dates convenient to you within two weeks after consultation with the second examiner so as to arrange for the conduct of Viva-Voce examination. Further, you are requested to provide the dates such that there should be a minimum of 10 days duration from the date of this orders. You will be paid TA/DA and Honorarium as per the University norms.

Yours faithfully

DIRECTOR

Encl: Xerox Copies of thesis evaluation reports.



E-Mail ID: drnd.vivavoce@jntua.ac.in  
Phone & Fax: 08554-273006  
Mobile: 9000 551420

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR**  
**ANANTHAPURAMU-515 002(A.P) INDIA**  
**Research & Development**

**Dr. A. Ananda Rao**  
Professor of CSE &  
Director

**CONFIDENTIAL**

To

**Lr.Rc.No.603-18/Ph.D.823/NRV/EEE/2018, Dt :28.03.2019**

1. **Dr. B.V. Sanker Ram,**

Professor,  
Dept. of Electrical & Electronics Engineering,  
JNTU College of Engineering, Kukatpally,  
Hyderabad-500085  
Cell: **8500961061**  
Email Id: **bvsram4321@yahoo.com**

**[Internal Examiner]**

2. **Dr. Yogendra Kumar,**

Professor, Dept. of EE,  
Moulana Nataional Institute of Technology,  
Bhopal-462003  
Cell: **08959662996**  
Email Id: **ykmact@yahoo.co.in**

**[External Examiner]**

Sir,

Sub: JNTUA – R & D – Ph.D. (EE) – Appointment of  
Examiners – Request for date of Viva-Voce examination – Reg.

Ref: D.O.Lr.No.660/ Ph.D./EEE-23/NRVM/EE /2018, dt: 27.03.2019.

\*\*\*

I am happy to inform you that you have been appointed as examiner for the conduct of Ph.D. Viva-Voce examination in respect of **Mr. N. Rajasekhar Varma** in the faculty of **Electrical & Electronics Engineering.**

Therefore, I request you to communicate any two dates convenient to you within two weeks after consultation with the second examiner so as to arrange for the conduct of Viva-Voce examination. Further, you are requested to provide the dates such that there should be a minimum of 10 days duration from the date of this orders. You will be paid TA/DA and Honorarium as per the University norms.

Yours faithfully

DIRECTOR

Encl: Xerox Copies of thesis evaluation reports.





**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR  
ANANTHAPURAMU – 515 002 (A.P.) – INDIA.**

**Prof. S. V. SATYANARAYANA**  
Director of Evaluation

I.r. No.674/Ph.D/EE-23/NUV/EE/2018 dt. 07.06.2019

**NOTIFICATION**

Sub:- Award of **Ph.D-Degree in Electrical Engineering** to **Mr. N.Rajasekhar Varma**

\*\*\*\*\*

The thesis entitled "**WAVELET TRANSFORM BASED HIGH IMPEDANCE FAULT DETECTION IN DISTRIBUTION SYSTEM USING ARTIFICIAL INTELLIGENCE AND KNN TECHNIQUES**" submitted by **Mr. N.Rajasekhar Varma**, [H.T.No.10Ph0220] has been accepted by the University on the recommendation of the panel of examiners. **Mr.N.Rajasekhar Varma** is provisionally declared and qualified for the award of **Doctor of Philosophy**, in the faculty of **Electrical Engineering**. The viva voce examination is conducted on **31.05.2019**.

Date: **07.06.2019**

//By Order//

  
**Director of Evaluation**

1. **Mr. N.Rajasekhar Varma** – through the Director, R&D, JNTUA, Ananthapuramu.
2. **Dr. B.V. Ssnker Ram**, Professor, Dept of Electrical & Electronics Engineering, JNTU College of, Engineering, Kukatpally, Hyderabad-500085. [Supervisor]
3. **Dr.Yogendra Kumar**, Professor, Dept EE, Moulana National Institute of Technology, Bhopal-462003.  
[External Examiner]
4. PA to Vice-Chancellor, Administrative Building, JNTUA, Ananthapuramu.
5. PA to Rector, Administrative Building, JNTUA, Ananthapuramu
6. PA to Registrar, Administrative Building, JNTUA, Ananthapuramu.
7. The Director, R&D, Administrative Building, JNTUA, Ananthapuramu.
8. The Director, Academic and Planning, Administrative Building, JNTUA, Ananthapuramu.
9. The Controller of Examinations (PG), Examination Branch, JNTUA, Ananthapuramu.
10. The Controller of Examinations (UG), Examination Branch, JNTUA, Ananthapuramu.
11. The Director Association of Indian Universities, Rouse Avenue, New Delhi.
12. The Public Relations Officer, Administrative Building, JNTUA, Ananthapuramu.



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
**HYDERABAD - 500 085, TELANGANA STATE, INDIA.**



320001517

SI. No. PC 00956857



**PROVISIONAL CERTIFICATE**



Hall Ticket No.: 1203PH0783

This is to certify that Mr. DURGAM KUMARASWAMY

son of Mr. PAPAIAH

has satisfied all the requirements for the award of Doctor of Philosophy

in the faculty of ELECTRICAL AND ELECTRONICS ENGINEERING

of this University, held in May, 2020 for the thesis entitled,

“MOSFET Based DC-DC Converter Using Soft Switching Techniques for Improving Performance”.

*Nelli*  
Verified by

Hyderabad - T.S.

Date: 12 June 2020

*Uppala*  
Director of Evaluation

*Qus*  
REGISTRAR





JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD  
(Established by JNTU Act No. 30 of 2008)  
Kukatpally, Hyderabad – 500 085, Telangana (India)

Dr. V KAMAKSHI PRASAD  
M.Tech., Ph.D. (IIT – Madras), FIE., MCSJ  
Professor of Computer Science and Engineering &  
DIRECTOR OF EVALUATION

Ref:- P7/Ph.D./1902/01-02/DK/EEE/2019  
Dt:- 12.06.2020

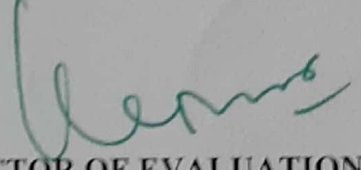
## NOTIFICATION

Sub : Award of Ph.D. Degree in Electrical & Electronics Engineering to Mr. Durgam Kumaraswamy (HT. No. 1203PH0783).

\* \* \*

The thesis entitled “MOSFET Based DC-DC Converter Using Soft Switching Techniques for Improving Performance” submitted by Mr. Durgam Kumaraswamy has been accepted by the University on the recommendations of the panel of examiners. Mr. Durgam Kumaraswamy is provisionally declared and qualified for the award of Doctor of Philosophy in the faculty of Electrical & Electronics Engineering.

//By order//

  
DIRECTOR OF EVALUATION

1. Mr. Durgam Kumaraswamy – through the Director, DRD, JNTUH, Kukatpally, Hyderabad.
2. Dr. B V Sanker Ram, Retd. Professor , H.No. 1-42/PE/111, Plot No. 111, Pragathi Enclave Colony Miyapur, Hyderabad – 500049, Telangana State. [Supervisor]
3. Dr. D. M. Vinod Kumar, Professor, Department of Electrical Engineering, National Institute of Technology Warangal, Warangal - 506 004, Telangana State. [External Examiner]
4. PA to Vice-Chancellor, Administrative Building, JNTUH, Kukatpally, Hyderabad.
5. PA to Registrar, Administrative Building, JNTUH, Kukatpally, Hyderabad.
6. The Director, DRD, Administrative Building, JNTUH, Kukatpally, Hyderabad.
7. The Director, Academic and Planning, JNTUH, Kukatpally, Hyderabad.
8. The Controller of Examinations, JNTUH, Kukatpally, Hyderabad.
9. The Director, Association of Indian Universities, Rouse Avenue, New Delhi.
10. The Public Relations Officer, Administrative Building, JNTUH, Kukatpally, Hyderabad.

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**

HYDERABAD - 500 085, TELANGANA STATE, INDIA

TS 1437999



H.T No. : **1103PH0734**

*Mr. Charansingh T*

*No. Sundarsingh*

having fulfilled the academic requirements in *June - 2020*  
has this day been admitted by the Executive Council to the Degree of

*Doctor of Philosophy*

*Electrical And Electronics Engineering*

Topic: "Transient Stability Analysis and Stability Improvement of Multi Machine System with 6 Phase Transmission Systems".

Given under the Seal of the University

Date: 16-10-2020



*Uppasani*

DIRECTOR OF EVALUATION

*[Signature]*

REGISTRAR

*[Signature]*

VICE CHANCELLOR



49214116933

PC No  
320001540





**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
(Established by JNTU Act No. 30 of 2008)  
Kukatpally, Hyderabad – 500 085, Telangana (India)

**Dr. V KAMAKSHI PRASAD**  
*M.Tech., Ph.D.( IIT – Madras), FIE., MCSI*  
*Professor of Computer Science and Engineering &*  
**DIRECTOR OF EVALUATION**

Ref:- P7/Ph.D./19256/10-01/CT/EEE/2019  
Dt : - 27.06.2020

## NOTIFICATION

Sub: Award of **Ph.D.** Degree in **Electrical & Electronics Engineering** to **Mr. Charansingh T,**  
(**HT. No. 1103PH0734**).

\* \* \*

The thesis entitled “**Transient Stability Analysis and Stability Improvement of Multi Machine System with 6 Phase Transmission Systems**” submitted by **Mr. Charansingh T** has been accepted by the University on the recommendations of the panel of examiners. **Mr. Charansingh T** is provisionally declared and qualified for the award of **Doctor of Philosophy** in the faculty of **Electrical & Electronics Engineering**.

//By order//

**DIRECTOR OF EVALUATION**

1. **Mr. Charansingh T** – through the Director, DRD, JNTUH, Kukatpally, Hyderabad.
2. Dr. B V Sanker Ram, Retd. Professor from JNTUH, H.No. 1-42/PE/111, Plot No. 111, Pragathi Enclave Colony, Miyapur, Hyderabad - 500 049, Telangana State. [ **Supervisor** ]
3. Dr. K Raghu Ram, Principal, Laqshya Inst. of Tech. & Science, Tanikella, Khammam - 507 305, Telangana State. [ **Co-Supervisor** ]
4. Dr. K. Shanti Swarup, Professor, Department of Electrical Engineering, Indian Institute of Technology Madras, Chennai - 600 036, Tamilnadu. [ **External Examiner** ]
5. PA to Vice-Chancellor, Administrative Building, JNTUH, Kukatpally, Hyderabad.
6. PA to Registrar, Administrative Building, JNTUH, Kukatpally, Hyderabad.
7. The Director, DRD, Administrative Building, JNTUH, Kukatpally, Hyderabad.
8. The Director, Academic and Planning, JNTUH, Kukatpally, Hyderabad.
9. The Controller of Examinations, JNTUH, Kukatpally, Hyderabad
10. The Director, Association of Indian Universities, Rouse Avenue, New Delhi.
11. The Public Relations Officer, Administrative Building, JNTUH, Kukatpally, Hyderabad.



ADMISSION LETTER

DR. A VINAYA BABU  
DIRECTOR, ADMISSIONS

Lr.No.DA/H3/Ph.D/M.Phil/MS/Adms(EX)/2011  
Dt.12.09.2011

SUB : JNTUH - Admissions to External Research Programmes - 2011

1. You are provisionally selected for admission into Ph.D. programme in ELECTRICAL ENGG. faculty subject to available of vacancy at the guide and/or co-guide.
2. You are requested to report to the Director, Research & Development Cell, JNTUH, Kukatpally, Hyd -85 on or before 01.10.2011 and submit the following
  - (i) P.C.Degree Certificate in Original (Provisional Certificate is not admissible)
  - (ii) Transfer Certificate in original
  - (iii) Fees:  
A Demand Draft drawn on any nationalized bank for Rs.20,700/- (Rs.20,000/- one year fee + Rs.500/- caution deposit + Rs.200/- admission fee) in favour of The Registrar, JNTUH, Hyderabad payable at Hyderabad.  
Regular University employees have to pay of Rs.200/- towards the admission fee.
  - (iv) Joining Report of the candidate, countersigned by the Research Supervisor(s) indicating the place where the Research work will be carried out.
  - (v) No Objection Certificate from the Head of the Organisation.
  - (vi) Latest Four passport size photographs.
3. Details of the Candidates:

(1) Name & Address: T CHARAN SINGH  
H NO 4-1-215  
ROTARY NAGAR  
KHAMMAM  
507002

Supervisor(s) Name  
Designation and Address  
DR B V SANKER RAM  
VICE-PRINCIPAL  
JNTUH COLLEGE OF ENGG  
KUKATPALLY  
HYDERABAD-85

Co\_Supervisor(s) Name  
Designation and Address  
DR K RAGHU RAM  
PRINCIPAL  
LAQSHYA INST OF TECH & SCIENCES  
TANIKELLA-507305  
KONIJERLA MDL, KHAMMAM

Topic of Research:  
ANALYSIS OF TRANSIENT STABILITY WITH SMART GRID AND 6 PHASE TRANSMISSION SYSTEMS

4. From second year onwards for each year you will have to pay an amount of Rs.20,000/- (Rupees twenty thousands only) towards yearly fee before July 31 and obtain receipt.

To  
200123 :  
T CHARAN SINGH

  
DIRECTOR, ADMISSIONS

Copy to:

- (1) The Director, R&D Cell, JNT University, Kukatpally, Hyderabad-85
- (2) Supervisor are requested to check the list of candidates (given in JNTUH website) admitted under them till last year and then accept newly admitted candidate only if the no.of candidates under them are less than eight. Submit your list of eight candidates along with acceptance letter to Director R&D Section, JNTUH. After verifying the no.of candidates the admission of new candidate will be approved.
- (3) Co-Supervisor with a request to accept only if the no.of scholars are less than eight.

Note: The candidate should collect copy of guidelines & academic regulations from the Director, R&D Cell, JNT University, Hyderabad.



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, TELANGANA STATE, INDIA

TS 1320928



H.T No.: 0603PH0722

*Mr. Dakka Obulesu*

*S/o. D Basaiah*

having fulfilled the academic requirements in *February, 2015*  
has this day been admitted by the Executive Council to the Degree of

*Doctor of Philosophy*  
*Electrical And Electronics Engineering*

Topic : "Design and Implementation of Robust Unified Power Flow  
Controller for Damping Power System Oscillations".

Given under the Seal of the University

Date:21-04-2017



DIRECTOR OF EVALUATION

REGISTRAR

VICE CHANCELLOR



4621400037

PC No  
25973121507





JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD  
(Established by Andhra Pradesh Act No. 30 of 2008)  
Kukatpally, Hyderabad – 500 085, Telangana (India)

Dr. K.ESHWARA PRASAD  
B.Tech.,M.Tech.,Ph.D.,MISTE,FIE,C.Eng.  
Professor of Mechanical Engg.  
DIRECTOR OF EVALUATION

Ref:- P7/Ph.D/1406/01-06/DO/EEE/2014  
Dt:- 06.02.2015

## NOTIFICATION

Sub: Award of Ph.D. Degree in Electrical & Electronics Engineering to Mr. Dakka Obulesu  
(HT. No. 0603PH0722).

\*\*\*\*

The thesis entitled "Design and Implementation of Robust Unified Power Flow Controller for Damping Power System Oscillations" submitted by Mr. Dakka Obulesu has been accepted by the University on the recommendations of the panel of examiners. Mr. Dakka Obulesu is provisionally declared and qualified for the award of Doctor of Philosophy in the faculty of Electrical & Electronics Engineering.

//By order//

  
DIRECTOR OF EVALUATION

1. Mr. Dakka Obulesu- through the Director, DRD, JNTUH, Kukatpally, Hyd.
2. Dr.S.F.Kodad, Professor and Principal, Krishnamurthy Institute of Technology &Engineering Survey No:23 & 39, Edulabad (V), Ghatkesar (M), Hyderabad-501301. [Supervisor]
3. Dr.B.V.Sanker Ram, Professor, Dept. of EEE, JNTU College of Engineering, Kukatpally, Hyderabad – 85. [Co-Supervisor]
4. Dr. K. Shanti Swarup, Professor, Dept. of Electrical Engineering, Indian Institute of Technology Madras Chennai - 600 036, Tamilnadu. [External Examiner]
5. PA to Vice-Chancellor, Administrative Building, JNTUH, Kukatpally, Hyderabad.
6. PA to Registrar, Administrative Building, JNTUH, Kukatpally, Hyderabad.
7. The Director, Directorate of R & D, Administrative Building, 3<sup>rd</sup> Floor, JNTUH, Kukatpally, Hyderabad.
8. The Director, Academic and Planning, JNTUH, Kukatpally, Hyderabad
9. The Controller of Examinations, JNTUH, Kukatpally, Hyderabad.
10. The Director Association of Indian Universities, Rouse Avenue, New Delhi.
11. The Public Relations Officer, Administrative Building, JNTUH, Kukatpally, Hyderabad.



UNDER CERTIFICATE OF POSTING  
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY  
KUKATPALLY : HYDERABAD 500 072

DR. M CHANDRA SHEKAR  
Coordinator, Admissions

Lr.No.CA/HS/MS/Ph.D/M,Phil/Adms(PT)/2006  
Dt.08.08.2006

SUB : JNT University - Admissions to Part Time Research Programmes - 2006

1. You are provisionally selected for admission into Ph.D. programme in ELECTRICAL ENGINEERING faculty
2. You are requested to report to the Director, Research & Development Cell JNT University, Kukatpally, Hyderabad-72 on or before 08.09.2006 and submit the following:
  - (i) P.G.Degree Certificate in Original (Provisional Certificate is not admissible)
  - (ii) Transfer Certificate in original
  - (iii) Fees:  
A Demand Draft drawn on any nationalized bank for Rs.20,700/- (Rs.20,000/- one year fee + Rs.500/- caution deposit + Rs.200/- admission fee) in favour of The Registrar, JNTU, Hyderabad payable at Hyderabad.  
Regular University employees have to pay of Rs.200/- only towards the admission fee
  - (iv) Joining Report of the candidate, countersigned by the Research Supervisor(s) indicating the place where the Research work will be carried out.
  - (v) No Objection Certificate from the Head of the Organisation.

3. Details of the Candidates:

- (i) Name & Address:  
D OBULESU  
TEACHER  
ZPHS, KAMALAPURAM  
KADAPA DIST  
516289
- (ii) Faculty in which admitted into Ph.D. programme : ELECTRICAL ENGINEERING
- (iii) Centre for Pre-Ph.D. Exam:  
THE DIRECTOR, RESEARCH & DEVELOPMENT CELL, JNT UNIVERSITY, KUKATPALLY, HYDERABAD-72

Supervisor(s) Name  
Designation and Address

DR S F KODAD  
PROFESSOR & HOD  
DEPT OF EEE  
AURORAS ENGG COLLEGE  
BHONGIR-506116

Co\_Supervisor(s) Name  
Designation and Address

DR B V SANKER RAM  
PROFESSOR  
DEPT OF ELECTRICAL ENGG  
JNTU COLLEGE OF ENGG  
KUKATPALLY, HYD-72

Topic of Research:  
DESIGN AND IMPLEMENTATION OF ROBUST UNIFIED POWER FLOW CONTROLLER FOR DAMPING POWER SYSTEM OSCILLATION

4. From second year onwards for each year you will have to pay an amount of Rs.20,000/- (Rupees twenty thousand only) towards yearly fee before July 31 and obtain receipt.

To  
D OBULESU ✓

COORDINATOR, ADMISSIONS

Copy to: (1) The Director, R&D Cell, JNT University, Kukatpally, Hyderabad-72  
(2) Supervisor  
(3) Co-Supervisor

Note: The candidate should collect copy of instructions & admission regulations from the Director, R&D Cell, JNT University, Hyderabad.



## CERTIFICATE

This is to certify that the thesis entitled "***Design and implementation of robust unified power flow controller for damping power system oscillations***" that is being submitted by **Sri. Dakka.Obulesu [Reg. No. 0603PH0722]** in partial fulfillment for the award of Ph.D. in Electrical & Electronics Engineering to the Jawaharlal Nehru Technological University Hyderabad, is a record of bonafide work carried out by him under our guidance and supervision.

The results embodied in this thesis have not been submitted to any other University or Institute for the award of any degree or diploma.



**Signature of Co-Supervisor  
Name and Designation**

**Dr. B.V. SANKER RAM**

M.Tech., Ph.D., M.I.S.T.E., D.I.M.,  
Professor

Dept. of Electrical & Electronics Engg.,  
JNTU COLLEGE OF ENGINEERING  
HYDERABAD - 500 085.



Dr. S. F. Koded

**Signature of Supervisor  
Name and Designation**

**PRINCIPAL**

**KRISHNAMURTHY INSTITUTE OF  
TECHNOLOGY & ENGINEERING**

Sy. No: 23 & 39, Edulabad (V), Ghatkesar (M),  
R.R. Dist, (Hyd.)-501 301. Ph: 08415-260303

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

RF 400091  
31181506838



430001130713



*Mr. Isanaka Prabhakar Reddy*

*S/o Mr. Esanaka Penchala Reddy*

having fulfilled the academic requirements in *September - 2011*  
has this day been admitted by the Executive Council to the Degree of

*Doctor of Philosophy*

*Electrical and Electronics Engineering*

Topic: 'Control Strategy and Placement of Facts Devices for Damping of  
Power System Oscillations.'

Given under the Seal of the University

H. T. No: 410745EE/PH

Date: 07-05-2012

DIRECTOR OF EVALUATION

REGISTRAR

VICE-CHANCELLOR






## CERTIFICATE

This is to certify that the thesis entitled "**Control Strategy and Placement of FACTS Devices for Damping of Power System Oscillations**", that is being submitted by **Isanaka Prabhakar Reddy, 410745EE/PH** in partial fulfillment for the award of the degree of **Doctor of Philosophy in Electrical and Electronics Engineering** to the **Jawarlal Nehru Technological University Hyderabad** is a record of bonafide work carried out by him under my guidance and supervision.

The results have been verified and found satisfactory. The results embodied in this thesis have not been submitted to any other University or Institute for the award of any degree or diploma.

 <sup>cm</sup>  
21/7/2010

Supervisor

**Dr. B.V. SANKER RAM**

Professor

Dept. of Electrical & Electronics Engg.

JNTUH College of Engg. Hyderabad,

JNTU Hyderabad, Kukatpally,

Hyderabad - 500 085, AP, India





**Directorate of Evaluation**  
**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA**  
**KAKINADA-533003, Andhra Pradesh, INDIA**

**Dr. P. Subba Rao,**  
*B.Tech., M.E., Ph.D.,*  
**Professor of Civil Engineering &**  
**Director of Evaluation**

Lr. No. DE / Ph.D / 237 /EEE/ D. Srinivas (09022P0216) / 2016, Dt: 26.10.2016.

**NOTIFICATION**

Sub: Award of Ph. D Degree in faculty of Electrical & Electronics Engineering to Mr. D. Srinivas -  
Reg.

\*\*\*\*\*

The Thesis entitled "A Novel Method for Balancing of DC Link Capacitor Voltages of Multilevel Inverters" submitted by Mr. D. Srinivas, Roll No. 09022P0216 in Department of Electrical & Electronics Engineering has been accepted by the University. Based on the recommendations of the Panel of Examiners; Mr. D. Srinivas is provisionally declared qualified for the award of degree of **Doctor of Philosophy** in faculty of Electrical & Electronics Engineering.

Date: 26-10-2016.

// By Order//

**Director of Evaluation**

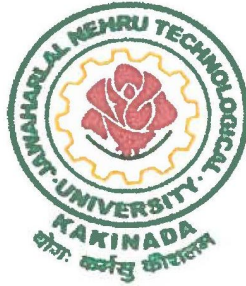
Copy to

1. Mr. D. Srinivas, Research Scholar – through the Director, R&D Cell, JNTUK, Kakinada.
2. Dr. B. Sarvesh, External Examiner Department of EEE, JNTU College of Engineering, JNTUA, Anathapur -515 002.
3. Dr. Dr. B.K Sankar Ram, Professor of EEE, University College of Engineering, JNTUH, Kukatpally, Hyderabad.
4. The Secretary to the Hon'ble Vice Chancellor JNTUK, Kakinada.
5. The Rector, JNTUK, Kakinada.
6. The Registrar, JNTUK, Kakinada.
7. The Director, R&D Cell, JNTUK, Kakinada.
8. The Director, Academic & Planning, JNTUK, Kakinada.
9. The Controller of Examinations, JNTUK, Kakinada.
10. The Director, Association of Indian Universities, Rouse Avenue, New Delhi.
11. The Public Relations Officer, JNTUK Kakinada.



**Department of Electrical and Electronics Engineering**  
**University College of Engineering, Kakinada**  
**(Autonomous)**

**J.N.T. University Kakinada**  
**Kakinada – 533003, A.P, India**



**Certificate**

This is to certify that the thesis entitled "**A NOVEL METHOD FOR BALANCING OF DC LINK CAPACITOR VOLTAGES OF MULTILEVEL INVERTERS**" is being submitted by **Mr.D Srinivas** (R.No: 09022P0216) in partial fulfillment for the award of **Doctor of Philosophy in Electrical and Electronics Engineering** to the Jawaharlal Nehru Technological University Kakinada is a record of bonafide work carried out by him under our guidance and supervision.

The results embodied in this thesis have not been submitted to any other University or Institute for the award of any degree or diploma.

**Signature of the Supervisor**

Dr.B.V.Sanker Ram,  
 Professor, Department of EEE  
 JNTU,Hyderabad

**Dr. B.V. SANKER RAM**  
 M.Tech., Ph.D., M.I.S.T.E., I  
 Professor  
 Dept. of Electrical & Electronics En  
 JNTU COLLEGE OF ENGINEE  
 HYDERABAD - 500 085





Directorate of Research and Development  
Jawaharlal Nehru Technological University Hyderabad  
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Lr. No. M/DRD/Ph.D./VSR/EEE/Viva-Voce/119/2010-2014, Dt. 05-12-2019.

Sub: JNTUH - DRD - Conduct of Ph.D. Viva-Voce-Examination - Reg.

Ref: 1) Academic Regulations & Guidelines DRD.

2) Order No. JNTUH/R&D/Ph.D. Viva/Rem/2010, Dt. 30-3-2010.

3) Order No. JNTUH/ Procds No: D1/3268/2011, Dt. 13-7-2011.

4) JNTUH D.O. Lr. No.P7/ Ph.D/19119/04-35/VSR/EEE/2019, Dt. 16-09-2019.

5) Lr. No. M/DRD/Ph.D./VSR/EEE/Viva-Voce/119/2010-2015, Dt.30-09-2019.

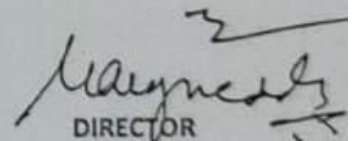
6) Letter from Dr. K Shanti Swarup (External Examiner), Dt.03-12-2019.

-oOo-

As per the reference cited, the Viva-Voce examination of Ph.D. Research Scholar Mr. Vantipull Sanjeeva Rao (H.T. No. 1203PH0715), in the faculty of Electrical & Electronics Engineering will be conducted on **13-12-2019 (FN/AN)** at Seminar Hall, Dept. of Electrical & Electronics Engineering, JNTUH, Kukatpally, Hyderabad. The Research Scholar is therefore requested to report to the Head, Dept. of Electrical & Electronics Engineering, JNTUH on the date mentioned above, to take the Viva-Voce examination. Further the following guidelines may be followed at the time of Viva-voce examination for defending his/her Thesis.

1. He/She should first briefly explain the Thesis work, (using LCD Projector/Overhead Projector) and has to answer the questions raised by the examiners.
2. Description of experimental model of the setup should be kept ready in the form of Photos / figures for the verification of the examiners.
3. Any other visual display, which he/she wishes to exhibit, in connection with Ph.D. Thesis may be displayed, in the Hall.
- 4) The Research Scholar should contact the HOD, one day before to ensure the logistic arrangements.
- 5) The Candidate should bring the thesis copy for Viva – Voce Exam.

The Viva-Voce will be kept open for the faculty, Scholars and students of the relevant discipline. Further he/she is requested to report at least one hour before the commencement of Viva-Voce Examination.

  
DIRECTOR

To

**Mr. Vantipull Sanjeeva Rao**

(Electrical & Electronics Engineering) Research Scholar.

CC to 1) The Principal, College of Engineering, JNTUH.

2) The Vice Principal with a request to circulate the same to all Depts.

3) The Head, Dept. of Electrical & Electronics Engineering, College of Engg., JNTUH.

Note: 1) The Candidate has to apply for Provisional Certificate after Successful Completion of Ph.D Viva – Voce Exam.(PC Application available in ->Examinations ->Downloads)

2) To receive the PC the candidate has to bring the necessary documents available in the Formats, Check List and List of Enclosures -> R&D ->Downloads

3) The Scholar has to submit a Hard Copy thesis to the HOD for Dept Library on the day of Viva-Voce.

## CERTIFICATE

This is to certify that the thesis entitled "**IMPROVEMENT OF POWER SYSTEM STABILITY BY PSS BASED FACTS CONTROLLERS USING EVOLUTIONARY ALGORITHMS**" that is being submitted by **Sri. SANJEEVA RAO VANTIPULI** in partial fulfillment for the award of Ph.D. in **ELECTRICAL ENGINEERING** to the **Jawaharlal Nehru Technological University Hyderabad, Kukatpally, Hyderabad (T.S.) - 500 085, India**, is a record of bonafide work carried out by him under our guidance and supervision.

The results embodied in this thesis have not been submitted to any other University or Institute for the award of any degree or diploma.

Signature of Supervisor



**Dr B V SANKER RAM**  
Professor

Dept. of Electrical & Electronics Engineering,  
JNTU College of Engineering  
Kukatpally, Hyderabad-72.





**DRAVIDIAN UNIVERSITY**  
Off Campus Research Programme  
**KUPPAM – 517 425 :: A.P.**

DU/OCRP/Ph.D/2008-09

Date: 21.04.2017

**REVISED PROCEEDINGS**

**Sub:** Dravidian University, Kuppam - Registration of Candidates for Off campus Part Time – Admission to Ph.D, 2008-09 batch – Change of Guide - Regarding.

**Ref:** 1) Proceedings DU/OCRP/Ph.D/2008-09.  
2) Your letter dated 21.04.2016

\*\*\*

Mr/Ms. **M. Balaraju** has been admitted into Ph.D, degree as an Off Campus Part-time Research Scholar in **Physics** for 2008-09 batch with Reg.No **01509223005/2008-09** and date of admission 31.05.2009.


**Topic of Research Topic:** "Study on Ultrasonic Propagation in Polymers and Molecular Interaction Studies of Binary System Using Ultrasonic Interferometer Technique."

**Name of the Previous Research Supervisor:** Dr. E. Venkateswara Rao.

**Name of the Present Research Supervisor:** Dr. K. Madhusudhana.

His/her registration is governed by the Ph.D Regulations, 2009.

The rules and regulations remains the same as mentioned in the earlier proceedings vide reference 1<sup>st</sup> cited.

  
24/4/17  
DIRECTOR

To  
Copy to the Candidate  
Copy to the Research Supervisor  
Copy to file



# Annamalai University

(A STATE UNIVERSITY ACCREDITED WITH 'A' GRADE BY NAAC)

ANNAMALAINAGAR - 608 002

Ph.D. Degree Programme

ADMISSION MEMO

(2016-2017)



App. No. : R21350  
Date : 31/10/16  
Programme Code : 3607

For Office Use - Roll Number

16

Mr./Ms. VINOTHKUMAR J is informed that he/she has been provisionally selected for admission to Ph.D. Degree Programme in Ph.D [Computer Science & Engineering] under External registration, subject to fulfil the eligibility conditions.



The Candidate is registered under the guidance of

Name of the Guide	Name of the Co-Guide
Dr. K. VENKATACHALAPATHY PROFESSOR COMPUTER SCIENCE & ENGINEERING	Dr. R. NAKKEERAN PROFESSOR & PRINCIPAL PUDUCHERRY CHRIST INSTITUTE OF TEHCNOLOGY, RAMANATHAPURAM, PUDU

He / She has to produce all the required original certificates at the time of admission.

1. 10th, H.Sc., U.G., P.G., and M.Phil Mark Lists
2. Degree Certificate - P.G. & M.Phil Programmes
3. Transfer or Migration Certificate
4. Research VISA & Passport, if Foreign National
5. Recent Passport Size Photographs (2 Numbers)

He / She has to report to 'K' Section, Administrative office, Annamalai University on 07/11/16 at 10.00 A.M.

His / Her admission will be confirmed, after verification of original certificates by 'K' section and on payment of Tuition Fee of Rs.77400.00. He should submit joining report along with copy of remittance challan through proper channel.

Note: Other conditions viz., Submission of titles, Course Work Examinations, Extension of time, Change of guide/status, Submission of thesis, etc., remains as prescribed in the prospectus.

To

Mr./Ms. VINOTHKUMAR J

Copy To :

The Head, Department of COMPUTER SCIENCE & ENGINEERING

The Guide : Dr. K. VENKATACHALAPATHY, PROFESSOR, COMPUTER SCIENCE & ENGINEERING

The Co - Guide : Dr. R. NAKKEERAN, PROFESSOR & PRINCIPAL

PUDUCHERRY CHRIST INSTITUTE OF TEHCNOLOGY, RAMANATHAPURAM, PUDUCHERRY

Note : Payment of Fee be considered as Date of Joining

*K. Arjun*  
**REGISTRAR**  
*Prof. J.*



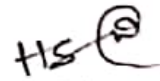
## **DECLARATION BY THE CANDIDATE**

I declare that the thesis entitled "A STUDY ON DYNAMIC RESPONSE IN HYDROLIC EQUIPMENT WORKING WITH HEAVY LOADS" is my own work conducted under the supervision of **DR. NILESH DIWAKAR** at Sri Satya Sai University of Technology & Medical Sciences, Sehore, MP and Co-Supervisor of **DR. B NAGESHWAR RAO**, Mahaveer Institute of Science & Technology, Hyderabad approved by Research Degree Committee. I have put in more than 240 days of attendance with supervisor at the center.

I further declare that to the best of my knowledge, the thesis does not contain any part of any work which has been submitted for the award of any degree either in this University or in any other University without proper citation.

Place :Sehore(MP)

Date:



Signature of the Candidate

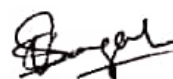
## **DECLARATION BY THE CANDIDATE**

I declare that the thesis entitled "**OPTIMIZATION OF SPRAYING PROCESS IN THERMAL POWER PLANT USING ROBOT**" is my own work conducted under the supervision of **DR. NILESH DIWAKAR** at Sri Satya Sai University of Technology & Medical Sciences, Sehore, MP and Co-Supervisor of **DR. B NAGESHWAR RAO**, Mahaveer Institute of Science & Technology, Hyderabad approved by Research Degree Committee. I have put in more than 240 days of attendance with supervisor at the center.

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Place :Sehore(MP)

Date:



**Signature of the Candidate**



***Study on Ultrasonic propagation in Polymers and Molecular  
Interaction Studies of Binary System using Ultrasonic  
Interferometer Technique***

*Thesis submitted to Dravidian University in partial fulfillment  
of the requirements for the degree of Doctor of Philosophy in Physics*

By

**M.BALARAJU  
(REG No. 01509223005)**

***Research Supervisor***

**Dr. K. MADHUSUDHANA**

Assistant Professor,  
Mahaveer Institute of Science and Technology  
Bandlaguda, Hyderabad – 500 005.



**DRAVIDIAN UNIVERSITY  
KUPPAM, ANDHRA PRADESH  
(2017)**